

# VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad – 500 090, Telangana, India

## TIMETABLE FOR I M.TECH, I SEMESTER (R25), SESSIONAL EXAMINATION-II – MARCH, 2026

Branch	9 <sup>th</sup> March 2026 10:00 A.M. to 12:00 Noon	10 <sup>th</sup> March 2026 10:00 A.M. to 12:00 Noon	11 <sup>th</sup> March 2026 10:00 A.M. to 12:00 Noon	12 <sup>th</sup> March 2026 10:00 A.M. to 12:00 Noon	13 <sup>th</sup> March 2026 10:00 A.M. to 12:00 Noon	16 <sup>th</sup> March 2026 10:00 A.M. to 12:00 Noon
	Monday	Tuesday	Wednesday	Thursday	Friday	Monday
<b>STR</b>	ADVANCED STRUCTURAL ANALYSIS (25PC1ST01)	THEORY OF ELASTICITY (25PC1ST02)	STRUCTURAL DYNAMICS (25PC1ST03)	THEORY OF PLATES AND SHELLS (25PE1ST01) PRECAST TECHNOLOGY (25PE1ST02)	ADVANCED CONCRETE TECHNOLOGY (25PE1ST06)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) [COMMON TO ALL]
<b>HE</b>	PAVEMENT MATERIAL CHARACTERIZATION (25PC1HW01)	TRAFFIC ENGINEERING (25PC1HW02)	HIGHWAY DESIGN (25PC1HW03)	URBAN TRANSPORTATION PLANNING (25PE1HW01)	REMOTE SENSING AND GIS APPLICATIONS IN HIGHWAY INFRASTRUCTURE (25PE1HW05)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) [COMMON TO ALL]
<b>GTE</b>	ADVANCED SOIL MECHANICS (25PC1GT01)	ADVANCED FOUNDATION ENGINEERING (25PC1GT02)	GROUND IMPROVEMENT TECHNIQUES (25PC1GT03)	ENGINEERING ROCK MECHANICS (25PE1GT02)	SUBSURFACE INVESTIGATIONS AND INSTRUMENTATION (25PE1GT07)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) [COMMON TO ALL]
<b>PE</b>	POWER ELECTRONIC CONVERTERS (25PC1PL01) [COMMON TO PE, PS]	MODELING AND ANALYSIS OF ELECTRICAL MACHINES (25PC1PL02)	ELECTRIC DRIVES SYSTEM (25PC1PL03)	RENEWABLE ENERGY TECHNOLOGIES (25PE1PS01) [COMMON TO PE, PS]	MACHINE LEARNING APPLICATIONS (25PE1PL06) [COMMON TO PE, PS]	RESEARCH METHODOLOGY AND IPR (25MN6HS01) [COMMON TO ALL]
<b>PS</b>	POWER ELECTRONIC CONVERTERS (25PC1PL01) [COMMON TO PE, PS]	ADVANCED POWER SYSTEM ANALYSIS (25PC1PS01)	POWER SYSTEM DYNAMICS (25PC1PS02)	RENEWABLE ENERGY TECHNOLOGIES 25PE1PS01 [COMMON TO PE, PS]	MACHINE LEARNING APPLICATIONS (25PE1PL06) [COMMON TO PE, PS]	RESEARCH METHODOLOGY AND IPR (25MN6HS01) [COMMON TO ALL]
<b>CAD</b>	ADVANCED CAD (25PC1CD01)	CNC TECHNOLOGIES AND PROGRAMMING (25PC1CD02)	FLEXIBLE MANUFACTURING SYSTEMS (25PC1CD03) [COMMON TO CAD, AMS]	DESIGN FOR MANUFACTURING AND ASSEMBLY (25PE1AM04) [COMMON TO CAD, AMS]	AUTOMATION IN MANUFACTURING (25PC1AM01) [COMMON TO CAD, AMS]	RESEARCH METHODOLOGY AND IPR (25MN6HS01) [COMMON TO ALL]

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AMS	ADVANCES IN CAD/CAM (25PC1AM02)	ADVANCED MANUFACTURING PROCESSES (25PC1AM03)	FLEXIBLE MANUFACTURING SYSTEMS (25PC1CD03) <b>[COMMON TO CAD, AMS]</b>	DESIGN FOR MANUFACTURING AND ASSEMBLY (25PE1AM04) <b>[COMMON TO CAD, AMS]</b>	AUTOMATION IN MANUFACTURING (25PC1AM01) <b>[COMMON TO CAD, AMS]</b>	RESEARCH METHODOLOGY AND IPR (25MN6HS01) <b>[COMMON TO ALL]</b>
ES	RTL SIMULATION AND SYNTHESIS (25PC1VS01) <b>[COMMON TO ES, VLSI]</b>	PROCESSORS FOR EMBEDDED SYSTEM DESIGN (25PC1ES01)	PROGRAMMING LANGUAGES FOR EMBEDDED SOFTWARE (25PC1ES02)	WIRELESS COMMUNICATION AND NETWORKS (25PE1ES02)	ADVANCED DIGITAL SIGNAL PROCESSING (25PE1ES07)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) <b>[COMMON TO ALL]</b>
VLSI	RTL SIMULATION AND SYNTHESIS (25PC1VS01) <b>[COMMON TO ES, VLSI]</b>	DIGITAL IC DESIGN (25PC1VS02)	ANALOG IC DESIGN (25PC1VS03)	ADVANCED DIGITAL SYSTEM DESIGN (25PE1VS02)	SOC DESIGN (25PE1VS05)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) <b>[COMMON TO ALL]</b>
SE	SOFTWARE QUALITY ASSURANCE AND TESTING (25PC1SE01)	ADVANCED DATA STRUCTURES AND ALGORITHMS (25PC1CP02) <b>[COMMON TO SE, CSE, AIDS]</b>	MACHINE LEARNING (25PC1CP03) <b>[COMMON TO SE, CSE]</b>	DEVOPS (25PE1SE02)	SOFTWARE RELIABILITY AND QUALITY MANAGEMENT (25PE1CP07) <b>[COMMON TO SE, CSE]</b>	RESEARCH METHODOLOGY AND IPR (25MN6HS01) <b>[COMMON TO ALL]</b>
CSE	DATA SCIENCE (25PC1CP01)	ADVANCED DATA STRUCTURES AND ALGORITHMS (25PC1CP02) <b>[COMMON TO SE, CSE, AIDS]</b>	MACHINE LEARNING (25PC1CP03) <b>[COMMON TO SE, CSE]</b>	DISTRIBUTED COMPUTING (25PE1CP01)	SOFTWARE RELIABILITY AND QUALITY MANAGEMENT (25PE1CP07) <b>[COMMON TO SE, CSE]</b>	RESEARCH METHODOLOGY AND IPR (25MN6HS01) <b>[COMMON TO ALL]</b>
EI	TRANSDUCERS AND SMART INSTRUMENTS (25PC1LI01)	SIGNAL CONDITIONING CIRCUITS (25PC1LI02)	PROCESS CONTROL INSTRUMENTATION (25PC1LI03)	POWER PLANT INSTRUMENTATION (25PE1LI04)	VLSI TECHNOLOGY, FABRICATION AND DESIGN (25PE1LI06)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) <b>[COMMON TO ALL]</b>
CNIS	ADVANCED COMPUTER NETWORKS (25PC1CN01)	CRYPTOGRAPHY AND NETWORK SECURITY (25PC1CN02)	ADVANCED PROGRAMMING (25PC1CN03)	DESIGN OF INTERNET OF THINGS (25PE1CN01)	ARTIFICIAL INTELLIGENCE AND APPLICATIONS (25PE1CN09)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) <b>[COMMON TO ALL]</b>

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DS	MACHINE LEARNING MODELS (25PC1DC01)	FINANCIAL ANALYTICS AND DATA SCIENCE FOR BFSI (25PC1DC02)	MATHEMATICAL AND STATISTICAL METHODS IN DATA SCIENCE (25PC1DC03)	APPLIED STATISTICAL LEARNING (25PE1DC01)	ENSEMBLE LEARNING AND GRAPH ANALYTICS (25PE1DC02)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) [COMMON TO ALL]
AIDS	ARTIFICIAL INTELLIGENCE AND INTELLIGENT SYSTEMS (25PC1AS01)	ADVANCED DATA STRUCTURES AND ALGORITHMS (25PC1CP02) [COMMON TO SE, CSE, AIDS]	STATISTICAL FOUNDATIONS FOR DATA SCIENCE (25PC1AS02)	AGENTIC AI (25PE1AS03)	GENERATIVE AI (25PE1CP10)	RESEARCH METHODOLOGY AND IPR (25MN6HS01) [COMMON TO ALL]



**Controller of Examinations**

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- Note :**
1. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE EXAMINATION BRANCH IMMEDIATELY
  2. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS PER SCHEDULE.